Appl. No. 09/521,641 Amdt. dated Jan. 14, 2005 Reply to Office action of July 16, 2004

Amendments to the Specification:

Replace paragraph [0039] of the Substitute Specification with the following corrected paragraph:

[0039] To represent ε , an unsigned sixteen-bit mantissa m is combined with an unsigned exponent e, biased so that the actual represented value is $\varepsilon=2^{2^{-1}}e^{m}$. Thus, the exponent is also the right shift amount necessary to correct a 16b x 16b \rightarrow 32b multiply with ε as an operand. The two in the exponent allows ε to range from 0 to 4 when m is interpreted as a fractional amount and f ranges between zero and the Nyquist frequency.